

Attorney Docket No.: 0180151

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Labelle, et al.

Serial No.: 10/705,347

Filing Date: November 8, 2003

For: Method for Integrating a High-K
Gate Dielectric in a Transistor
Fabrication Process

Art Unit: 1765

Examiner: CHEN, Kin Chan

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*reconsideration only.
(no amendment)*

KCC

2/13/06

RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the *Final* Office Action dated December 7, 2005 in
the above-referenced patent application. Please enter and consider the following
remarks.